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RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE - EXAMINING GROUP 2631  
Attorney's Docket No. RA9-98-074/4269-61

PATENT Official

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Abdelilah et al.

Serial No.: 09/264,085

Filed: March 8, 1999

For: RECEIVERS, METHODS, AND COMPUTER PROGRAM PRODUCTS FOR AN  
ANALOG MODEM THAT RECEIVES DATA SIGNALS FROM A DIGITAL  
MODEM

Confirmation No.: 2597

Group Art Unit: 2631

Examiner: P. Kumar

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Date: February 10, 2003

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Commissioner for Patents

Washington, DC 20231

SECOND RESPONSE AFTER FINAL ACTION  
PURSUANT TO 37 C.F.R. §1.116

Sir:

This is a full and timely response to the final Office Action mailed October 11, 2002 and the Advisory Action mailed December 30, 2002 that contains amendments and remarks as set forth hereafter. Attached hereto is a marked up version of the changes made to the claims by the current amendments. The marked up version of the changes is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE."

In the Claims:

Please enter amended Claims 1 and 3 as follows:

1. (Twice Amended) A receiver for demodulating a data signal transmitted from a digital source at a network sampling rate that is synchronized with a network clock, comprising:

a two-stage interpolator, responsive to digital samples of the data signal, that generates interpolated digital samples in response thereto, the digital samples having a first local sample rate that is synchronized with a local clock and the interpolated digital samples having a second local sample rate that is synchronized with the network clock, the two-stage interpolator comprising:

a time converter, responsive to a sampling index signal, that generates first and second integers in response thereto;

a polyphase interpolator, responsive to the digital samples of the data signal